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Betker 8-8-16

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Betker et al.
Case: 8-8-16
Serial No.: 10/081,308
Filing Date: February 20, 2002
Group: Unassigned
Examiner: Unassigned

"Express Mail" Label No.: EL862103713US

I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231

Signature: Date: April 8, 2002

Title: Method and Apparatus for Establishing a Bound on the Effect of Task Interference in a Cache Memory

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying PTO Form 1449. A copy of the listed item is enclosed.

1. Kaxiras et al, "Cache Decay: Exploiting Generational Behavior to Reduce Cache Leakage Power," IEEE, (2001).
2. Lai et al., "Dead-Block Prediction & Dead-Block Correlating Prefetchers," IEEE, (2001).
3. Mendelson et al, "Modeling Live and Dead Lines in Cache Memory System," IEEE, Trans. on Computers, v. 42, no. 1, (January 1993).

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,

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Date: April 8, 2002

FORM PTO-1449 (MODIFIED)**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

Applicant: Betker et al.
Case: 8-8-16
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Group: 2185

U.S. PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION

OTHER DOCUMENTS

EXAMINER	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

____ Kaxiras et al, "Cache Decay: Exploiting Generational Behavior to Reduce Cache Leakage Power," IEEE, (2001).

____ Lai et al., "Dead-Block Prediction & Dead-Block Correlating Prefetchers," IEEE, (2001).

____ Mendelson et al, "Modeling Live and Dead Lines in Cache Memory System," IEEE, Trans. on Computers, v. 42, no. 1, (January 1993).

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.